



VLSID 2019

The Go Green Conference



Important dates:

Submission of abstract:
30 November 2018

Acceptance notification:
10 December 2018

Final Presentation Due:
19 December 2018

Conference:
7th - 9th January 2019

CALL FOR USER/DESIGNER TRACK ABSTRACTS

32nd International Conference on VLSI Design and 18th International Conference on Embedded Systems is a forum for researchers and designers to present and discuss current topics in VLSI design, electronic design automation, embedded systems, and emerging technologies. The User/Designer Track at VLSID'19 invites abstract submissions from industry and academia in the following areas.

TOPICS OF INTEREST

- Intelligence on Silicon
- Embedded Systems
- Security and Privacy
- IoT and Cyber-Physical Systems
- Digital Design
- Design Automation Algorithms
- Analog and Mixed Signal
- RF Design
- Power and Energy
- CMOS Technology and Devices
- Test and Verification
- Emerging Technologies

SUBMISSIONS

All abstract submissions to the User/Designer Track should be made electronically via the conference website (https://www.softconf.com/i/vlsid2019_user/) by 30 November 2018. Your abstract should clearly state the novel ideas, results, and applications of the contribution. Submissions will undergo a double-blind review. Abstract must be in PDF format and not exceed 2 single-spaced pages including figures and references in two-column IEEE conference paper format (10pt font). Abstracts exceeding the page limit or identifying the authors will be rejected without review.